

BIOGRAPHIES



ENRICO VILLA, Chair Micro- Nanoelectronics, KETs Sherpa Group

Enrico Villa became Executive Vice President of STM with effect as of January 1, 2005, following the creation of the Corporate Executive Committee, comprised of six corporate executives in addition to the COO and the CEO.

In addition to his varied functions within STM, Enrico Villa has been involved with several industry associations. From 1987 to 1994 he was Chairman of Electronic Components Group within the Associazione Nazionale Industrie Elettrotecniche ed Elettroniche (ANIE), the Italian Association of Electrical and Electronic Companies. Between 1993 and 1996 he was President of EECA, the European Electronic Component Manufacturers Association, and since 2003 has been re-elected to this position annually. He is Vice President of the European Semiconductor Industry Association (ESIA); furthermore, he is currently Chairman of the European Semiconductor Council within ESIA, as well as Chairman for Europe of the Joint Steering Committee for the World Semiconductor Council.

In February 2008 Enrico Villa was named Chairman of the Board of MEDEA+ and CATRENE, the two largest electronics R&D research clusters for micro/nano-electronics in Europe.



GEORGETTE LALIS, Director Industrial Innovation and Mobility Industries, DG Enterprise, European Commission

Georgette Lalis is Director in the European Commission's Directorate General of Enterprises and Industry (Directorate D). Her responsibilities include industrial innovation, the automotive, defence, aerospace and maritime industries.

Mrs Lalis joined the European Commission in 1981 as a junior official and prior to her present job she has held several managerial and senior positions in different sectors of the Institution. She has been a member of Commissioner Papandreu Cabinet (1989-1992) as well as Director for Maritime Transport (1998-2001).

Mrs Lalis, has studied law at the Universities of Athens and Strasbourg.



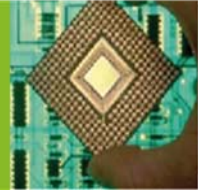
DR. REINHARD PLOSS, Member of the Management Board, Executive Vice President, Infineon Technologies

Reinhard Ploss joined Siemens/Infineon in 1986 as a process engineer for ion implantation in wafer manufacture at Munich, focusing on process development, yield and quality improvement. After about 1 year he assumed responsibility for ion implantation process technology and later also for epitaxy.

KETs OPEN DAYS

- Micro/Nanoelectronics-

October 18, 2010



In 1992 he moved to Villach, where he was initially responsible for the transfer of the epitaxy process and later for the coordination of the product transfer. In 1996 he went back to Munich and took charge of the Power Semiconductor Business Unit with a focus on development and manufacturing. Additionally, he was appointed as President of eupec GmbH Co. KG in 1999.

Since 2000 he headed the Automotive & Industrial Business Group, which then encompassed power semiconductors, electric drives, automotive applications and the Microcontroller Business Unit. In 2005 Reinhard Ploss assumed responsibility for manufacture, development and operational management in the Automotive, Industrial and Multimarket Business Group.

June 2007, Dr. Reinhard Ploss became a member of the management board and is now responsible for Operations comprising central manufacturing Frontend and Backend, Test, Quality Management as well as Logistics.



ANDRÉ-JACQUES AUBERTON-HERVÉ, CEO, SOITEC; Chair EU Semi

Dr. André-Jacques Auberton-Hervé co-founded Soitec with Jean-Michel Lamure in 1992. As President, CEO and Chairman of the Board, Auberton-Hervé is responsible for overseeing the strategic, operational and financial activities of the company.

Prior to founding Soitec, Auberton-Hervé managed several joint development programs between Leti and Thomson-CSF, which focused on the technological transfer from R&D to production applications. These programs included the transfer of 1.2 μm and 0.8 μm SOI CMOS for space applications. He was also in charge of several projects, which applied SOI to 3D integration, VLSI and ULSI.

In July of 2005, Auberton-Hervé was elected to serve as the European representative to SEMI's International Board of Directors. His demonstrated vision, experience and leadership capabilities within the industry contributed to his appointment to the board. Auberton-Hervé is also the recipient of the 1999 European SEMI Award in recognition of his work on Smart Cut™ technology and his overall contributions to the semiconductor industry as a whole. Auberton-Hervé is a member of the Electrochemical Society and the IEEE.

Auberton-Hervé holds a doctorate degree in semiconductor physics and a master's degree in materials science from *Ecole Centrale de Lyon* (France).



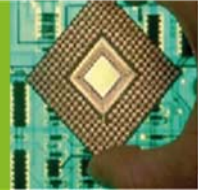
ROEL KRAMER, President, Point One.

Roel Kramer commenced his Philips career in 1970 by joining Philips Research, after having graduated in 1968 in experimental physics at the University of Amsterdam, the Netherlands. In 1982 Roel Kramer became group leader (Vice-President) of Philips Research, responsible for MOS devices and technology, followed two years later by his promotion to Director (Senior Vice-President) of Philips Research, where his main responsibility focused on semiconductor

KETs OPEN DAYS

- Micro/Nanoelectronics-

October 18, 2010



technology.

Due to his membership in the FOM Council for Semiconductors (a Dutch Scientific Council) and his contacts with various universities and academic institutes, he co-established the Submicron Institute in Delft, the Netherlands.

In 1990, Roel Kramer joined Philips Semiconductors as chief technology officer (CTO), and in 1995 he was appointed managing director (Executive Vice-President). In 1997, he became chief technology officer of Philips Consumer Electronics. In his role as CTO, he was responsible for the overall technological direction throughout the CE group of businesses. Roel Kramer is co-founder of the successful Philips Software Centre in Bangalore, India.

Since his retirement from Philips in 2003, Mr. Kramer has his own Innovation Consultancy Company.



HARALD GRUBER, Head of Division, ICT and e-Economy European Investment Bank.

Harald Gruber is head of the ICT and e-Economy division at the Projects Directorate of the European Investment Bank based in Luxembourg. He oversees project appraisal and has contributed to the Bank's strategy papers on the ICT sector, including semiconductors, and the knowledge economy in general. He has been professor at Bocconi University (Milan) for telecommunications economics.

He has published books such as *The Economics of Mobile Telecommunications* (Cambridge University Press) and *Learning and Product Innovation. Theory and Evidence from the Semiconductor Industry* (North Holland) and a wide range of articles in particular on the ICT sector and semiconductors in refereed journals.

He is on the editorial board of *Telecommunications Policy and Information Economics and Policy*. Harald Gruber holds a Ph.D. in Economics from the London School of Economics.



OLAF BABINET, Senior Manager, Deloitte Consulting LLP

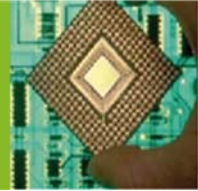
Mr. Babinet, Senior Manager, Franco-Dutch, joined Deloitte 12 years ago. His role is to manage global location strategy and site selection engagements all across

EMEA, North America and Asia. Mr. Babinet has more than 15 years experience advising companies with facility location strategy and/or restructuring operations, primarily for manufacturing. Over the past five years, Olaf has developed a specific knowledge in advising high technology projects including in the semiconductor and solar industries. Olaf has also developed incentives and grants advice to large companies interested in setting up operations in the enlarged European Union, Middle East, Asia, and in the US. During the course of his many projects, Mr. Babinet has developed professional relationships with representatives of most development agencies and relevant countries for investment in the world.

KETs OPEN DAYS

- Micro/Nanoelectronics-

October 18, 2010



PETER SCHERRER, General Secretary, European Metalworkers' Federation. EMF

Peter Scherrer studied history, sociology and journalism at the Free University of Berlin and the University of Newcastle-upon-Tyne (UK). He has a Masters in History from the University of Bielefeld. He began his career in 1974 when he underwent training to become a skilled metalworker (apprenticeship). In 1990 he was appointed International Secretary, press officer and assistant to the President of GGLF (horticultural, agricultural and forestry workers' trade union).

He was head of the International Affairs Department of the Hans Böckler Foundation (HBS) in Düsseldorf from July 1992 until August 1994. In 1998 he became the editor of the English-language quarterly publication South-East Europe Review for Labour and Social Affairs (SEER), published by the Hans Böckler Foundation and the European Trade Union Institute (ETUI).

In January 2003 he worked for two years as Policy secretary (steel industry) in the Executive Board branch office of IG Metall in Düsseldorf.

Peter Scherrer is responsible for the overall representation and coordination of Press and Public relations, Finances, Personnel, Relations with employers, ETUC, IMF, EIFs, Industrial Policy and Company Policy.



DIRK HILBERT, Vice Mayor of Dresden

Since September 2001, Dirk Hilbert is the Deputy Mayor and Head of Department of Economic Development of the City of Dresden. He is a member of the German Liberal Party (FDP). He graduated as an industrial engineer at the Dresden University of Technology in 1998. From March 1998 until September 2000 he was working as assistant of a member of the Executive Board and deputy office manager at the German Aerospace Center in Cologne. Before this experience he had worked for four years for the department of business studies at the Dresden University of Technology, especially activities related to production management, innovation management and evaluation and grading of technologies.



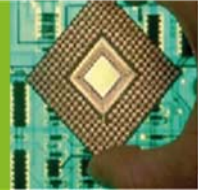
GENEVIÈVE FIORASO, Member of the French Parliament; Vice president of Grenoble Alpes Metropole; Deputy Mayor in charge of Economy, Employment, University and Research.

Geneviève Fioraso is member of the French Parliament, deputy mayor in charge of Economy, Employment, University and Research, City of Grenoble, 1st vice-president of the Metro, the Grenoble urban area council (26 cities), in charge of economic development and CEO of the SEM Minatec Entreprises.

KETs OPEN DAYS

- Micro/Nanoelectronics-

October 18, 2010



Former professional experiences, all dedicated to development, both in public and private sectors: marketing manager, France Telecom (2000-2004), director of the Regional Agency for the development of digital research and industry in the Rhone-Alpes Region (1999-2000), director of cabinet for Michel Destot, mayor of Grenoble (1995-1999), deputy manager of Corys, a start-up from the French Nuclear Energy Commission (CEA of Grenoble), dedicated to software simulation and modelization (1989-1995), responsible of an association in charge of professional insertion, teacher in professional training organisms.



JIM O'HARA, General Manager, Intel Ireland Vice President, Technology Manufacturing Group (TMG), Intel Corporation

Jim O'Hara is vice president, Technology Manufacturing Group, Intel Corporation and Intel Ireland general manager.

Jim took over the role of Intel Ireland general manager in April 2002 and is responsible for Intel's manufacturing operations in Ireland and for the site's strategic direction, management leadership and development, and government and community relations.

O'Hara joined Intel in 1991 as part of the Fab 10 start up team. He became plant manager for Fab 10 in 1996 and subsequently managed both Fab 10 and Fab 14, now known as Ireland Fab Operations.

Prior to joining Intel Jim worked in Digital Equipment Corporation for the previous 17 years, the last five of which were based in their corporate headquarters in Massachusetts.

O'Hara is a member of both the management and governing boards for ICT Ireland. OHara has previously served on the boards of the Smurfit Business School at University College Dublin, the Institute Board of CRANN (Centre for Research on Adaptive Nanostructures and Nanodevices) at Trinity College Dublin and as a member of the Irish Research Council for Science Engineering and Technology (IRCSET). In July 2008 he joined the board of Business in the Community Ireland. In October 2009 Jim OHara was appointed to the Board of Enterprise Ireland by An Tánaiste (Ireland's Deputy Prime Minister) and the Minister for Enterprise, Trade and Employment. Enterprise Ireland is the government agency responsible for the development and promotion of the indigenous business sector.

O'Hara was conferred with an honorary degree of Doctor of Science by the National University of Ireland.



ORIO BELLEZZA, Executive Vice President, General Manager, Front-End Manufacturing, STMicroelectronics

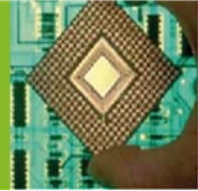
Orio Bellezza is Executive Vice President and General Manager of STMicroelectronics' Front-End Manufacturing, responsible for the Company's wafer fabrication operations and facilities, and has held this position since January 2008. He is a member of ST's Corporate Strategic Committee.

Bellezza joined SGS-ATES, a predecessor company to STMicroelectronics, in 1984. He soon moved to the Company's Central R&D organization and participated in several key projects, including the introduction of process technology modules for manufacturing sub-micron non-volatile memories. In

KETs OPEN DAYS

- Micro/Nanoelectronics-

October 18, 2010



1996, Bellezza was appointed Director of ST's R&D facility in Agrate and led its upgrade and expansion into the Company's development center for non-volatile memory and smart-power technologies. In 2002, he became Vice President of Central R&D, and in 2005, was promoted to Vice President and Assistant General Manager of Front-End Technology and Manufacturing.

Bellezza has published technical papers and earned several patents in non-volatile memories.

Orio Bellezza was born in Bergamo, Italy, in 1959, and graduated with a degree in Chemistry from the University of Milan (Università degli Studi di Milano), Italy.



LUC VAN DEN HOVE, CEO, IMEC

Luc Van den hove was till June 30, 2009 Executive Vice President and Chief Operating Officer at imec (Leuven, Belgium). He became Chief Executive Officer (CEO) on July 1, 2009. He joined imec in 1984, starting his research career in the field of silicide and interconnect technologies. In 1988, he became manager of IMEC's micro-patterning group (lithography, dry etching); in 1996, Department Director of Unit Process Step R&D; and in 1998, Vice-President of the Silicon Process and Device Technology Division. In January 2007, he was appointed as imec's EVP & COO. Luc Van den hove received his Ph.D. in Electrical Engineering from the University of Leuven, Belgium. He has authored or co-authored more than 100 publications and conference contributions.



OLIVIER VATEL, Vice President Technology Development division, Global Foundries

Olivier Vatel is Vice President of Technology Module Development at GlobalFoundries. He joined the corporation earlier this year and his responsibilities include module/unit process development across the multiple sites. Olivier is based in Saratoga County, Upstate New-York, USA.

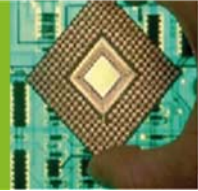
Before joining GlobalFoundries, Olivier spent over 15 years at Motorola/Freescale in different functions/sites. After a start in wafer fab in Toulouse-France and the start-up of SC300 in Dresden-Germany, Olivier did move to Process manufacturing operation in Austin-Texas. Returning to Europe for the start-up of the Crolles2-Alliance where he held the position of Director of Unit Process Development. Last, Olivier was in charge of a design collateral development organization in Austin-Texas.

Olivier was born in Chalons en Champagne, France, in 1966, and received PhD in Material Science from the University of Aix-Marseille II and did perform an post-PhD period at NTT research laboratories in Japan.

KETs OPEN DAYS

- Micro/Nanoelectronics-

October 18, 2010



TUDOR BROWN, President, ARM

Tudor Brown was one of the founders of ARM. Before joining the Company, he was at Acorn Computers where he worked on the ARM R&D programme. He joined the board in 2001 and became President in 2008 with responsibility for developing high-level relationships with industry partners and governmental agencies and for regional development. His previous roles include Engineering Director and Chief Technology Officer, EVP Global Development and Chief Operating Officer. He is a Fellow of the Institution of Engineering and Technology and is a non-executive director of ANT plc. He currently

sits on the UK Government China Task Force.



LAURENT MALIER, Director CEA-LETI

Laurent MALIER is heading the LETI – Laboratory for Electronics and Information Technology-, a research institute based in France, within MINATEC pole of excellence. With a staff of 1,200 researchers, the LETI develops technologies and solutions in nanoelectronics, microtechnologies and their applications, specifically in telecoms, ambient intelligence, imaging, biology and healthcare.

With a portfolio of 1,400 patents, the LETI is focused on applied research; it develops and transfers miniaturized devices, based on its 200 mm and 300 mm silicon infrastructures.

Laurent MALIER was graduated from Ecole Polytechnique and received his M.S. degree in Solid State Physics in 1990, from University of Paris-Orsay. His thesis research focused on nanoporous oxides and their applications, and he pursued research on semiconductor nanocrystals, fullerenes and optical glasses. In 1996, he joined the French ministry of Defense, in charge of R&D strategy and programs in electronics, where he launched developments in uncooled infrared imagers and high-power gallium nitride RF devices. He was at the time chairing the European cooperation of DODs in electronic components. In 2001, he joined ALCATEL optronics, a public company producing optoelectronic components for optical telecommunications, in charge of new products developments. He took over the semiconductor business unit of ALCATEL Optronics, which became AVANEX in 2003. Within AVANEX, he continued to lead the semiconductor business unit where he developed the foundry activity.

Laurent MALIER joined the LETI in 2006, first as VP strategy and programs.



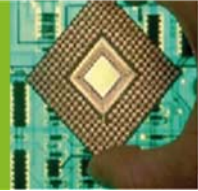
ENRICO SANGIORGI, University of Bologna; Vice Chair Scientific Community Council ENIAC; Member Steering Board AENEAS

In 1983, 1984, and 1991, he was a Visiting Scientist at the Center for Integrated Systems, Stanford University, Stanford, California, for approximately three years. From 1985 to 2001, he was a consultant at Bell Laboratories-Lucent Technologies, Murray Hill, NJ, where he was a

KETs OPEN DAYS

- Micro/Nanoelectronics-

October 18, 2010



Resident Visitor for more than three years. In 1993, he was appointed Full Professor of Electronics at the University of Udine, Italy, where he started the Electrical Engineering Program and the microelectronic group. In 2002, he joined the University of Bologna, where he is currently in charge of the nano- microelectronics group in the satellite Campus of Cesena-Forli (II School of Engineering).

Since 2005 he is the Director of IU.NET (Italian Universities Nanoelectronic Team), a Legal Consortium grouping eight University Groups active in the field of Nanoelectronics. Since 1994 he is Editor of IEEE Electron Device Letters. He has been a member of the Technical Committees of several International Conferences on Electron Devices: IEDM ('91-96; '04-'06), ESSDERC ('99-present), INFOS ('95-03), ULIS ('00-present), etc.

Enrico Sangiorgi is a Fellow of the IEEE, Distinguished Lecturer of the Electron Device Society, Chairman of the Electron Device Society TCAD Technical Committee. He has been involved in several European Projects of the V and VI FP with Management Responsibilities. He is currently a member of the Executive-Scientific Committee of the SINANO Network of Excellence, managing the modeling activities, and a member of the Strategic Board of the Integrated Project PULLNANO. Since 2006 he is the Vice Chairman of the Scientific Community Council (SCC) of the ENIAC Technology Platform.



FRANCOIS ESCHER, Director Public Affairs, Marvell; Co-Chair ESIA Competitiveness TF

During his professional career, Francois held a number of senior management positions: Prior to his current assignment as a Director, Public Affairs, Europe, with Marvell Semiconductor; he was Director Government Relations EMEA with Freescale Semiconductor; Director for Strategy & Marketing with Motorola Semiconductor Europe (SPS) and General Manager of Motorola Switzerland; Director for Strategic Capabilities and New Business Development with AT&T International Operations; Director of Corporate Strategy, Alliances, and Organizational Capability Europe with Digital Equipment Corporation; Head of Socio-Economic Research and Consulting with Battelle Research Centers, Geneva, leading the business strategy and future prospective practice.

Francois graduated in international relations, political science and economics at the University of Geneva IUHEI (MA). He also holds a degree in social sciences and cultural anthropology from the University of Neuchatel (BA).



GERARD BEENKER, Scientific Director and Manager of External Relations NXP Semiconductors

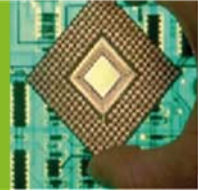
Gerard Beenker joined Philips Research Laboratories in Eindhoven in 1980. Since 1989 he has had several management positions in the sector IC Design of Philips Research Eindhoven. On January 1st 1999 he was appointed Senior Vice President of Philips Research. From Jan 1999 till Feb 2006, he has been responsible for the sector IC Design in Philips Research, combined with his role as manager of the IC program and account manager for Philips Semiconductors.

In 2006 he established the NXP Research organisation, and was appointed as manager of the NXP Research organisation. In November 2008, he became Scientific

KETs OPEN DAYS

- Micro/Nanoelectronics-

October 18, 2010



Director of NXP and manager of NXP External Relations. In this role he particularly focuses on developing and managing external relations with scientific, R&D community and public authorities, reflecting the growing importance of external partnerships for leveraging NXP's R&D portfolio. In the fall of 2009 he was elected member of the Presidium of Artemisia. He is active in various advisory boards and councils, amongst others he is member of the Point-One Program Council in The Netherlands.



PROF. DR. GUO QI ZHANG, Delft University of Technology, Fellow and Sr. Director for SSL open innovation program of Philips Lighting

As an active and renewed player in international Micro/Nanoelectronics and SSL arena, Prof. Zhang has authored/co-authored more than 170 scientific publications; been invited as keynote speaker by many int. events; chaired /co-chaired several international conferences (EuroSimE, ICEPT, ChinaSSL, Micro/NanoReliability Congress, etc.); been serving as member of editorial board for 3 international journals and editor-in-chief for book

series "SSL technologies and Applications".

Prof. Zhang has been making active contribution to the definition and execution of public R&D strategy and programs, such as: chair for "More than Moore" Strategic Research Agenda of Eniac; coordinator of Strategic Research Agenda of Dutch national innovation program Point-One; member of expert and strategy consulting group of China SSL Alliance.



LOÏC LIÉTAR, Executive Vice President & Chief Strategy Officer, STMicroelectronics

Loïc Liétar is an Executive Vice President and Chief Strategy Officer of STMicroelectronics, and has held this position since January 2008. He is responsible for the Company's Strategic Planning, Corporate Business Development, and Corporate Communication (since February 2010). Liétar is a member of ST's Corporate Strategic Committee and also sits on the Board of Directors of ST-Ericsson.

Liétar joined Thomson Semiconducteurs, a predecessor company to STMicroelectronics, in 1985. After working in R&D Management and Marketing, he was appointed Director of the Company's Advanced Systems Technology labs in the US in 1999. Four years later, Liétar became General Manager of ST's Cellular Terminals Division, and later moved to head the Application Processor Division. Appointed Group Vice President, Strategies in 2006, he contributed to deconsolidating ST's Flash memory business, as well as establishing the R&D partnership with IBM and creating ST-Ericsson, the wireless joint venture with Ericsson.

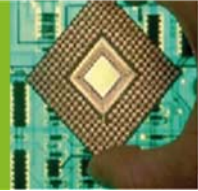
Liétar sits on the Board of Directors of the Global Semiconductor Alliance. In April 2010, he was elected President of the Minalogic micro-nanotechnology cluster in Grenoble, France.

Loïc Liétar was born in Paris, France, in 1962. He graduated with degrees in Engineering and Microelectronics from the École Polytechnique and Orsay University in Paris, respectively, and holds an MBA from Columbia University, New York.

KETs OPEN DAYS

- Micro/Nanoelectronics-

October 18, 2010



HUBERT LAKNER, Executive Director, Fraunhofer Institute for Photonic Microsystems IPMS, Dresden

Hubert Lakner (born 1958) received his diploma-degree in physics at the Eberhard-Karls-Universität in Tübingen in 1986. After a year in industry he joined the Gerhard-Mercator-Universität in Duisburg working in the field of nanocharacterization of mesoscopic semiconductor structures. There he received his Dr.-Ing. in 1993.

Since January 2003 he is the executive director of the Fraunhofer Institute for Photonic Microsystems (IPMS). In parallel, Dr. Lakner has been appointed Professor for »Optoelectronic Devices and Systems« in the Department of Electrical Engineering and Information Technology at the Technical University Dresden.

Since January 2005 he is also vice-speaker of the Fraunhofer Group Microelektronics.